L Number	Hits	Search Text	DB	Time stamp
1	197	712/205.ccls.	USPAT;	2003/07/07 14:18
	<b>.</b>		EPO; JPO	
2	148	712/206.ccls.	USPAT;	2003/07/07 14:22
-		.,	EPO; JPO	
3	296	712/207.ccls.	USPAT;	2003/07/07 14:22
			EPO; JPO	
12	27	(((microinstruction micro-instruction)	USPAT;	2003/07/07 14:26
		with buffer) same cache) and valid	EPO; JPO;	
			IBM TDB	
13	47	microinstruction near4 cache	USPĀT	2003/07/07 14:26
14	82	(microinstruction micro-instruction) with	USPAT;	2003/07/07 14:26
		queue	EPO; JPO;	
		1	IBM TDB	
15	46	((microinstruction micro-instruction) with	USPĀT;	2003/07/07 14:26
		queue) and valid	EPO; JPO;	
		,	IBM TDB	
16	14	(queue buffer) with (multithread\$4	USPĀT;	2003/07/07 14:27
		multi-thread\$4) with (instruction section	EPO; JPO	
		portion part)		
17	215		USPAT;	2003/07/07 14:27
+ '	213	buffer) and valid	EPO; JPO;	
			IBM TDB	
_	2	(microinstruction micro-instruction) with	USPAT;	2003/01/16 14:38
	2	queue with valid	EPO; JPO;	
		quodo with varia	IBM TDB	
_	2	("4682284" "5954815").pn.	USPAT;	2003/01/21 14:00
-	2	( 4002204 3334013 ).pm.	EPO; JPO;	2003/01/21 14:00
			IBM TDB	
_	0	08445563.ap.	USPAT;	2003/01/21 14:01
-	J	υυττυου.αμ.	EPO; JPO;	2000/01/21 14.01
	,		IBM TDB	
1_	, o	"08445563"	USPAT;	2003/01/21 14:01
-		00773303	EPO; JPO;	2003/01/21 14.01
			IBM TDB	
_	4	445563.ap.	USPAT;	2003/01/21 14:50
-	4	110000.αρ.	EPO; JPO;	2000,01/21 14.00
			IBM TDB	
_	1701	bypass with (queue buffer)	USPAT;	2003/01/21 14:50
-	1,01	Dibano with (duone parter)	EPO; JPO;	
			IBM TDB	]
_	0	bypass with (queue buffer) with	USPAT;	2003/01/21 15:02
-	0	(microinstruction micro-instruction)	EPO; JPO;	2000,01,21 10.02
		/micromiscraction micro-instruction/	IBM TDB	
_	5	bypass with (queue buffer) same	USPAT;	2003/01/21 15:02
_	,	(microinstruction micro-instruction)	EPO; JPO;	2000,01,21 10.02
		/microfficerdefoil micro filactuction/	IBM TDB	
	22	(queue buffer) with multithreaded	USPAT;	2003/01/22 16:00
_		/queue Durrer/ wrom murcremreaded	EPO; JPO	2003,01,22 10.00
_	· 118	microinstruction with cache	USPAT	2003/01/22 15:34
<del>-</del>	41	macroinstruction with cache	USPAT	2003/01/22 15:52
-	5	(microinstruction near4 cache) and	USPAT	2003/01/22 15:52
_	3	(macroinstruction near4 cache) and	OSERI	2003/01/22 13.32
	0	(queue buffer) with multithreaded with	USPAT;	2003/01/22 16:01
-	1		EPO; JPO	2003/01/22 10:01
	_	(portion section) (queue buffer) with multithreaded with	USPAT;	2003/01/22 16:02
-	0		EPO; JPO	2003/01/22 10:02
		(portion section part)		2003/01/22 16:04
-	2	(queue buffer) with multithreaded with	USPAT; EPO; JPO	2003/01/22 10:04
		(portion section partition\$4)	USPAT;	2003/01/23 10:28
-	3	1 . •		2003/01/23 10:28
	_	instruction	EPO; JPO	2003/01/22 00:50
-	0	712/205.ccls	USPAT;	2003/01/23 09:58
	<u> </u>	<u></u>	EPO; JPO	l